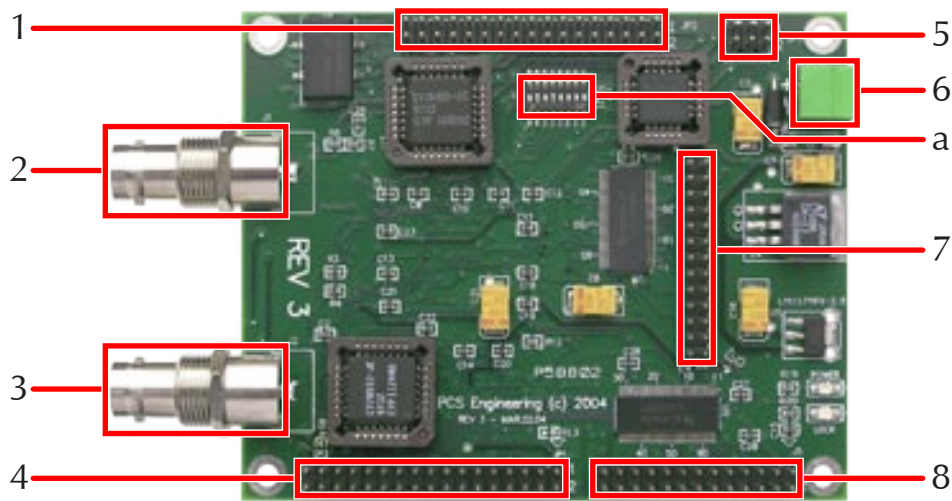


# ASI-Board

## Connector Description



board dimensions: 100×80 mm

|            |   |                           |                                             |
|------------|---|---------------------------|---------------------------------------------|
| Connectors | 1 | 34pin Header              | SPI (TS) Input                              |
|            | 2 | BNC Connector 75 $\Omega$ | ASI Input                                   |
|            | 3 | BNC Connector 75 $\Omega$ | ASI Output                                  |
|            | 4 | 34pin Header              | SPI (TS) Output                             |
|            | 5 | 6pin Header               | JTAG Connector<br>for programming the Flash |
|            | 6 | 2-way screw terminal      | Power Input<br>5 V DC only!                 |
|            | 7 | 26pin Header              | LVDS Input                                  |
|            | 8 | 26pin Header              | LVDS Output                                 |

Switches

a

8pin DIP-Switch

set operational mode

| Switch Setting |   |     |     |     |     |   |   | Operation                                                                                                                             |
|----------------|---|-----|-----|-----|-----|---|---|---------------------------------------------------------------------------------------------------------------------------------------|
| 1              | 2 | 3   | 4   | 5   | 6   | 7 | 8 | Result                                                                                                                                |
| on             | x | x   | x   | x   | x   | x | x | No PSI (PAT/PMT) Tables                                                                                                               |
| off            | x | x   | x   | x   | x   | x | x | PSI (PAT/PMT) Tables every 400 ms                                                                                                     |
| x              | x | off | on  | x   | x   | x | x | ASI Out (3) from ASI In (2) looped via hardware loop back<br><b>Note:</b> ASI In (2) can also connect to LVTTTL/SPI with Switches 5&6 |
| x              | x | on  | on  | x   | x   | x | x | ASI Out (3) from LVTTTL In (1)                                                                                                        |
| x              | x | on  | off | x   | x   | x | x | ASI Out (3) from SPI In (7)                                                                                                           |
| x              | x | x   | x   | off | on  | x | x | LVTTTL (4)/SPI (8) Out from ASI In (2)                                                                                                |
| x              | x | x   | x   | on  | on  | x | x | LVTTTL (4)/SPI (8) Out from LVTTTL (1) In via hardware loop back<br><b>Note:</b> ASI (3) Out with switches 3&4                        |
| x              | x | x   | x   | on  | off | x | x | LVTTTL (4)/SPI (8) Out from SPI (7) In via hardware loop back<br><b>Note:</b> ASI (3) Out with switches 3&4                           |